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Resource Management in Multicores— Finding and Handling the Bullies

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Problem

- Contention for shared resources is a major bottleneck for multicores—especially for mixed workloads.
- Modern processors implement instructions to manage caches, but they are typically unused.

Solution

- We propose a classification that allows us to predict application interference.
- We propose a method to automatically insert cache management instructions into misbehaving software.



Profile Run

We sample an application's memory accesses using a lightweight sampler. Using StatStack, we create a *stack* distance profile from the sample.

Memory Access Analysis

Miss? Using the stack distance profile we can determine if the data referenced by an instruction 0 will be reused after it is installed in the cache.

Miss Ratio Curves

Using the stack distance profile, we calculate the target application's miss ratio as a function of cache size. The miss ratio curve represents the application's cache behavior.





Compiler Feedback

We automatically disable caching for instructions that do not reuse data by injecting prefetchnta instructions into the compiler output.



Miss Ratio Curves

We calculate the new miss ratio and replacement ratio curves for the managed application using the stack distance profile and information from the analysis.

We define the cache sensitivity of an application, δ , to be the difference in miss ratio when running in isolation, $r_{\rm b}$, and when running only in the private cache, r_p. Using δ and r_h we can classify an application's cache behavior.

Classification





